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May 18, 2004

TO: Commissioner for Patents
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Total pages, including cover letter: 4

PTO FAX NUMBER 1-703-872-9306

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Title of OFFICIAL Document Transmitted:

Attached is a revised version of March 22, 2004 Response to Restriction Requirement, updated for changes in PTO format requirements

Applicant: KUSUMOTO
Serial No.: 09/773,502
Filed: 02/02/01
Group Art Unit: 2829
Our Ref. No.: 10873.644US01

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.


Sender's Name: Gina Dahl

May 18, 2004
Date

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S/N 09/773,502

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

Claims 1-66 are canceled.

67. (Withdrawn) A semiconductor device having a semiconductor integrated circuit, the semiconductor device comprising:
a plurality of leads for supplying the semiconductor device with a first power source from an outside;

a first internal power source line for supplying an internal circuit of the semiconductor integrated circuit with the first power source; and

a first plurality of internal terminals for supplying the first internal power source line with the first power source from the plurality of leads;
wherein all of the first plurality of internal terminals are connected to the first internal power source line via a first switching portion.

68. (Currently amended) ~~The A semiconductor device according to claim 67, further comprising another internal terminal that is connected directly to the first internal power source line not via the first switching portion.~~ having a semiconductor integrated circuit, the semiconductor device comprising:

a plurality of leads for supplying the semiconductor device with a first power source from an outside;

a first internal power source line for supplying an internal circuit of the semiconductor integrated circuit with the first power source; and

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a first plurality of internal terminals for supplying the first internal power source line with the first power source from the plurality of leads;

wherein one of the first plurality of internal terminals is connected directly to the first internal power source line, and others thereof are connected to the first internal power source line via a first switching portion.

69. (Withdrawn) The semiconductor device according to claim 67, comprising:
a second plurality of leads for supplying the semiconductor device with a second power source from the outside;

a second internal power source line for supplying the internal circuit with the second power source; and

a second plurality of internal terminals for supplying the second internal power source line with the second power source from the second plurality of leads;

wherein all of the second plurality of internal terminals are connected to the second internal power source line via a second switching portion.

70. (Currently amended) The semiconductor device according to claim 69 ~~68~~, further comprising ~~another internal terminal that is connected directly to the second internal power source line not via a switching portion.~~

a second plurality of leads for supplying the semiconductor device with a second power source from an outside;

a second internal power source line for supplying the internal circuit of the semiconductor integrated circuit with the second power source; and

a second plurality of internal terminals for supplying the second internal power source line with the second power source from the second plurality of leads;

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wherein one of the second plurality of internal terminals is connected directly to the second internal power source line, and others thereof are connected to the second internal power source line via a second switching portion.

71. (Withdrawn) The semiconductor device according to claim 69, further comprising a third switching portion provided between the first and second internal power source lines.

72. (Withdrawn) The semiconductor device according to claim 69, further comprising a current detecting circuit provided between the first and second internal power source lines.

73. (Currently amended) The semiconductor device according to claim ~~67~~ 68, wherein the internal circuit is a group of digital circuits.

74. (Currently amended) The semiconductor device according to claim ~~67~~ 68, wherein the first switching portion comprises a plurality of switching elements, and each of the first plurality of internal terminals is connected to the first internal power source line via a different switching element of the plurality of switching elements.